

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicants(s): Hao Fang  
Cameron C. Rabe  
Case: 2-4  
Serial No.: Unassigned  
Filing Date: Concurrently Herewith  
Group: Unassigned  
Examiner: Unassigned

Title: Impedance-Matched Write Circuit with Shunted Matching Resistor

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying PTO Form 1449.

**U.S. Patents**

1. U.S. Patent No. 6,121,800
2. U.S. Patent No. 6,512,646

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

Respectfully submitted,



Date: February 11, 2004

Kevin M. Mason  
Attorney for Applicant  
Reg. No. 36,597  
Ryan, Mason & Lewis, LLP  
1300 Post Road, Suite 205  
Fairfield, CT 06824  
(203) 255-6560

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**LIST OF PUBLICATIONS FOR  
APPLICANT'S INFORMATION  
DISCLOSURE STATEMENT**

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**U.S. PATENT DOCUMENTS**

EXAMINER					FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE
_____	6,121,800	09/19/00	Leighton et al.	327/110	
_____	6,512,646	01/28/03	Leighton et al.	360/46	

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**FOREIGN PATENT DOCUMENTS**

EXAMINER					TRANSLATION	
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES	NO
_____						
_____						
_____						
_____						
_____						
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**OTHER DOCUMENTS**

EXAMINER			
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.	
_____			
_____			
_____			

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Examiner

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Date Considered

**Examiner:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.